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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, and J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, and J. The cells are connected in a chain, with the output of one cell feeding into the input of the next cell to its right. The first cell (A) has an additional input from the left, and the last cell (J) has an additional output to the right.

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