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የጥቅም ስራ ማጠቃለያ



በዚህ ሰነድ ላይ የጥቅም ስራ ማጠቃለያ ይገኛል፡፡ ለጥቅም ስራ ማጠቃለያ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ፡

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የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ

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የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ

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የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ

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የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ



- የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ
- የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ
- የጥቅም ስራ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ ማጠቃለያ ሰነድ
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A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.



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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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A horizontal row of 10 boxes, each containing a digit from 1 to 10.



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A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row. The flip-flops are labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. Each flip-flop has a clock input (triangle symbol) and a data input/output. The output of Q9 is connected to the input of Q8, and so on, forming a chain. The output of Q0 is the final output of the register.

[illegible]

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A visual representation of the number 41 using base ten blocks. It consists of three ten rods and one one unit.



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1	2	3	4	5	6	7	8	9	10
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A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row. The flip-flops are labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. Each flip-flop has a clock input (triangle symbol) and a data input/output. The output of Q9 is connected to the input of Q8, and so on, forming a chain. The output of Q0 is the final output of the register.

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