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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, and J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, and J. The cells are connected in a chain, with the output of one cell feeding into the input of the next cell to its right. The first cell (A) has a feedback loop from its output back to its input. The last cell (J) has a feedback loop from its output back to its input.

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A horizontal number line with arrows at both ends. Below the line are 20 empty rectangular boxes for digits, arranged in two groups of 10. The first group of 10 boxes is for the tens place, and the second group of 10 boxes is for the ones place.

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A horizontal number line with arrows at both ends. Below the line are 20 empty boxes for digits, arranged in two groups of 10. The first group contains boxes for the tens and ones digits of the number, and the second group contains boxes for the tens and ones digits of the difference.

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A horizontal number line with arrows at both ends. Below the line are 20 empty rectangular boxes for digits, arranged in two groups of 10. The first group of 10 boxes is for the tens place, and the second group of 10 boxes is for the ones place.



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A horizontal number line with arrows at both ends. Below the line are ten empty rectangular boxes for digits, corresponding to the numbers 0 through 9.

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Diagram showing the arrangement of 10 boxes in two rows. The top row has 5 boxes and the bottom row has 5 boxes. The boxes are labeled with numbers 1 through 10.

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A horizontal number line with arrows at both ends. Below the line are 20 empty rectangular boxes for digits, arranged in two groups of 10. The first group of 10 boxes is for the tens digit, and the second group of 10 boxes is for the ones digit.

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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, and J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, and J. The cells are connected in a chain, with the output of one cell feeding into the input of the next cell to its right. The first cell (A) has an additional input from the left, and the last cell (J) has an additional output to the right.

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Diagram showing the arrangement of 10 boxes in two rows. The top row has 5 boxes and the bottom row has 5 boxes. The boxes are labeled with numbers 1 through 10.

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A horizontal number line with arrows at both ends. Below the line are 20 empty rectangular boxes for digits, arranged in two groups of 10. The first group of 10 boxes is for the tens digit, and the second group of 10 boxes is for the ones digit.

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The diagram shows a horizontal row of 10 rectangular boxes, each representing a bit in a shift register. The boxes are arranged in two groups of five. The first group (bits 1-5) is connected to a single input line on the left. The second group (bits 6-10) is connected to a single input line on the right. Each box has a small circle at the bottom, representing a flip-flop or storage element.

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A horizontal number line with arrows at both ends. Below the line are 20 empty boxes for digits, arranged in two groups of 10. The first group contains boxes for the tens and ones digits of the number, and the second group contains boxes for the tens and ones digits of the difference.

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A horizontal number line starting at 0 and ending at 20. Each integer from 0 to 20 is represented by a small square box. The boxes are empty, intended for students to write the corresponding number.

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