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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

□□□□ □□□ (1-10):

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A horizontal row of 10 boxes, each containing a number from 1 to 10.

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□□□□□□ (1-10):

A horizontal row of 10 square boxes. Each box contains a single digit from 1 to 10, in order from left to right.

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1	2	3	4	5	6	7	8	9	10
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[illegible]

A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a 'Q' output on the right and a 'D' input on the left. The flip-flops are labeled Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The 'D' input of the first flip-flop (Q11) is connected to the 'Q' output of the last flip-flop (Q0). The 'Q' output of the first flip-flop (Q11) is connected to the 'D' input of the second flip-flop (Q10). This pattern continues for all flip-flops, creating a serial shift register structure.

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□□□□□□ (1-10):

□□□□ □□□ (1-10):

A horizontal row of 10 boxes, each containing a digit from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

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1	2	3	4	5	6	7	8	9	10
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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

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□□□□□ □□□□ (1-10):

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A horizontal row of 10 boxes, each containing a digit from 1 to 10.

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A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a triangle on the right side. The flip-flops are labeled Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of each flip-flop is connected to the input of the next flip-flop to its right. The output of the last flip-flop (Q0) is connected to the input of the first flip-flop (Q11), forming a circular shift register.

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A horizontal row of 10 boxes, each containing a number from 1 to 10.

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A horizontal row of 10 boxes, each containing a digit from 1 to 10.

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

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1	2	3	4	5	6	7	8	9	10
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[illegible]

A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a 'Q' output on the right and a 'D' input on the left. The flip-flops are labeled Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The 'D' input of the first flip-flop (Q11) is connected to the 'Q' output of the last flip-flop (Q0). The 'Q' output of the first flip-flop (Q11) is connected to the 'D' input of the second flip-flop (Q10). This pattern continues for all flip-flops, creating a serial shift path. The 'Q' output of the final flip-flop (Q0) is labeled 'Q[0]'.

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

□□□□ □□□□ (1-10):

A horizontal row of 10 boxes, each containing a digit from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

□□□□□ □□□□ (1-10):

A horizontal number line with 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a row, with the numbers 1, 2, 3, 4, 5, 6, 7, 8, 9, and 10 in order from left to right.

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A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a 'Q' output on the right and a 'D' input on the left. The flip-flops are labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of Q9 is connected to the input of Q8, and so on, forming a chain. The output of Q0 is the final output of the register.

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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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A horizontal row of 10 boxes, each containing a digit from 1 to 10.

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□□□□ □□□ (1-10):

A horizontal row of 10 boxes, each containing a digit from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

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1	2	3	4	5	6	7	8	9	10
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A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a triangle on the right side. The flip-flops are labeled Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of each flip-flop is connected to the input of the next flip-flop to its right. The output of the last flip-flop (Q0) is connected to the input of the first flip-flop (Q11), forming a circular shift register.

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

□□□□ □□□ (1-10):

A horizontal row of 10 boxes, each containing a digit from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

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1	2	3	4	5	6	7	8	9	10
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A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a 'Q' output on the right and a 'D' input on the left. The flip-flops are labeled Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The 'D' input of the first flip-flop (Q11) is connected to the 'Q' output of the last flip-flop (Q0). The 'Q' output of the first flip-flop (Q11) is connected to the 'D' input of the second flip-flop (Q10). This pattern continues for all flip-flops, creating a serial shift path from right to left.

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□□□□□□ (1-10):

A horizontal row of 10 square boxes, each containing a number from 1 to 10 in order from left to right.

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1	2	3	4	5	6	7	8	9	10
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QUESTION

A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a triangle on the right side. The flip-flops are labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of each flip-flop is connected to the input of the next flip-flop to its right. The output of the last flip-flop (Q0) is connected to the input of the first flip-flop (Q9), forming a circular shift register.

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

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A horizontal row of 10 boxes, each containing a digit from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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1	2	3	4	5	6	7	8	9	10
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A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a 'Q' output on the right and a 'D' input on the left. The flip-flops are labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of Q0 is connected to the input of Q1, and so on, forming a chain. The output of Q9 is the final output of the register.

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10.

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□□□□□ □□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10.

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A horizontal row of 10 boxes, each containing a digit from 1 to 10.

[illegible]

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□□□□□□ (1-10):

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A horizontal row of 10 boxes, each containing a digit from 1 to 10. The boxes are arranged in a single row and are separated by small gaps.

□□□□□ □□□□ (1-10):

1	2	3	4	5	6	7	8	9	10
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[illegible]

A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a triangle on the right side. The flip-flops are labeled from left to right as Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0. The output of each flip-flop is connected to the input of the next flip-flop to its right. The output of the last flip-flop (Q0) is connected to the input of the first flip-flop (Q11), forming a closed loop.

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1 2 3 4 5 6 7 8 9 10

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1	2	3	4	5	6	7	8	9	10
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1 2 3 4 5 6 7 8 9 10

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

□□□□ □□□ (1-10):

□□□□□ □□□□ (1-10):

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A horizontal row of 10 boxes, each containing a digit from 1 to 10.

[illegible]

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□□□□□□ (1-10):

A horizontal row of 10 square boxes. Each box contains a single digit from 1 to 10, in order from left to right.

□□□□ □□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

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1	2	3	4	5	6	7	8	9	10
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[illegible]

A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a triangle on the right side. The flip-flops are labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of each flip-flop is connected to the input of the next flip-flop to its right. The output of the last flip-flop (Q0) is connected to the input of the first flip-flop (Q9), forming a circular shift register.

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□□□□□□ (1-10):

A horizontal row of 10 boxes, each containing a number from 1 to 10. The boxes are arranged in a single row, with numbers 1 through 10 in order from left to right.

□□□□ □□□ (1-10):

□□□□□ □□□□ (1-10):

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1	2	3	4	5	6	7	8	9	10
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A diagram of a 12-bit shift register. It consists of 12 flip-flops arranged in a horizontal row. Each flip-flop is represented by a rectangle with a 'Q' output on the right and a 'D' input on the left. The flip-flops are labeled Q11, Q10, Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. The output of Q11 is connected to the D input of Q10, and so on, forming a chain. The output of Q0 is labeled 'Q[11:0]'.

[illegible]

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□□□□□□ (1-10):

A horizontal row of 10 square boxes. Each box contains a single digit from 1 to 10, in order from left to right.

□□□□ □□□ (1-10):

A horizontal row of 10 square boxes, each containing a number from 1 to 10 in order from left to right.

□□□□□ □□□□ (1-10):

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1	2	3	4	5	6	7	8	9	10
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A horizontal row of 10 boxes, each containing a digit from 1 to 10.

The diagram shows a horizontal bus line with 12 connection points. On the left side, there are 12 input devices, each represented by a rectangle with a horizontal line at the top. On the right side, there are 12 output devices, each represented by a rectangle with a horizontal line at the bottom. The bus line connects each input device to its corresponding output device.

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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, and J from left to right. Below each cell is an output label: A, B, C, D, E, F, G, H, I, and J from left to right. Arrows indicate the data flow: from input A to the first cell, from input B to the second cell, and so on, up to input J to the tenth cell. Additionally, arrows show the shift operation: from the output of the first cell to the input of the second cell, from the output of the second cell to the input of the third cell, and so on, up to the output of the ninth cell to the input of the tenth cell. The output of the tenth cell is labeled J.

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□□□□□ □□□□ (1-10):

1 2 3 4 5 6 7 8 9 10

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1	2	3	4	5	6	7	8	9	10
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QUESTION How many different ways can you make \$100 using pennies, nickels, dimes, quarters, and half dollars?

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□□□□□□ (1-10):

□□□□ □□□ (1-10):

A horizontal row of 10 boxes, each containing a digit from 1 to 10.

□□□□□ □□□□ (1-10):

1	2	3	4	5	6	7	8	9	10
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