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1. 關於本計畫之執行進度與經費使用情形

(請於此處填寫)

請於本計畫執行期間，定期彙整執行進度與經費使用情形，並填具本表，於每月 15 日前，逕寄本會備查。

1

本計畫執行進度與經費使用情形，請於每月 15 日前，逕寄本會備查。

2

本計畫執行進度與經費使用情形，請於每月 15 日前，逕寄本會備查。

3

本計畫執行進度與經費使用情形，請於每月 15 日前，逕寄本會備查。

4

本計畫執行進度與經費使用情形，請於每月 15 日前，逕寄本會備查。



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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, and J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, and J. Arrows indicate the data flow: from input A to the first cell, from input B to the second cell, and so on, up to input J to the tenth cell. Additionally, arrows show the shift operation: from the output of the first cell to the input of the second cell, from the output of the second cell to the input of the third cell, and so on, up to the output of the ninth cell to the input of the tenth cell. The output of the tenth cell is labeled J.

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Age Group	Percentage
18-24	~15%
25-34	~15%
35-44	~15%
45-54	~15%
55-64	~15%
65-74	~15%
75-84	~15%
85+	~15%

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A row of ten vertical rectangular blocks. The first eight blocks are grouped together, representing 8 tens. The last five blocks are grouped together, representing 5 ones.

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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, J. The cells are connected in a chain, with the output of one cell being the input to the next cell to its right. The first cell (A) has a feedback loop from its output back to its input. The last cell (J) has a feedback loop from its output back to its input.

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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, and J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, and J. The cells are connected in a chain, with the output of one cell feeding into the input of the next cell to its right. The first cell (A) has a feedback loop from its output back to its input. The last cell (J) has a feedback loop from its output back to its input.

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ප්‍රශ්න 04 සිට 05 දක්වා පිළිතුරු ලියන්න

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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells, each representing a flip-flop. Above each cell is an input label: A, B, C, D, E, F, G, H, I, J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, J. The cells are connected in a chain, with the output of one cell feeding into the input of the next cell to its right. The first cell (A) has an additional input from the left, and the last cell (J) has an additional output to the right.

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ප්‍රශ්න 01 සිට 05 දක්වා පිළිතුරු ලියන්න



ප්‍රශ්න 01

ප්‍රශ්න 02 සිට 05 දක්වා

ප්‍රශ්න 02 සිට 05 දක්වා පිළිතුරු ලියන්න

ප්‍රශ්න 03 සිට 05 දක්වා

ප්‍රශ්න 03 සිට 05 දක්වා පිළිතුරු ලියන්න -- ප්‍රශ්න 03, ප්‍රශ්න 04, ප්‍රශ්න 05 පිළිතුරු ලියන්න

ප්‍රශ්න 04 සිට 05 දක්වා

ප්‍රශ්න 04 සිට 05 දක්වා පිළිතුරු ලියන්න

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ප්‍රශ්න 05 සිට 05 දක්වා පිළිතුරු ලියන්න

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