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A horizontal row of ten square boxes. The first box contains a colon (:). The subsequent nine boxes are numbered sequentially from 1 to 9.

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Diagram illustrating a shift register with 10 bits. The register is shown as a row of 10 boxes. The first 9 boxes are empty, and the 10th box contains the value 1. To the right of the 10th box is a colon followed by the numbers 1 through 10, each in its own box.

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Diagram of a 10-bit shift register. The register is represented by a horizontal row of 10 boxes. The first 9 boxes are empty, and the 10th box contains the value 10. To the right of the 10th box is a colon followed by the numbers 1 through 10, representing the bit positions.



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A horizontal row of 10 square boxes. The first box contains a colon (:). The remaining nine boxes are numbered 1 through 9 from left to right.



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A diagram consisting of ten adjacent rectangular boxes arranged horizontally. The first box contains a colon (:). Each of the remaining nine boxes contains a number from 1 to 9, respectively.



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A horizontal row of 10 rectangular boxes. The first box contains a colon ':'. The subsequent boxes are numbered 1 through 10.

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A horizontal row of 10 rectangular boxes. The first box contains a colon ':'. The remaining nine boxes are numbered 1 through 9 from left to right.

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Diagram illustrating a 10-bit shift register. The register is represented by a horizontal row of 10 boxes. The first 9 boxes are empty, and the 10th box contains the value 1. To the right of the 10th box is a colon followed by the numbers 1 through 10, each in its own box.



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